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Call for Papers

HPSR2004: Workshop on High Performance Switching and Routing

April 18-21, 2004, Phoenix, Arizona

<http://hpsr2004.asu.edu>

The scope of the High Performance Switching and Routing (HPSR) Workshop is focused on the technology of high-speed switches and routers, including architectural and algorithmic aspects of high-performance networks. HPSR 2004 will comprise of technical sessions for presentation of novel research results, panel sessions for discussion of hot topics, and tutorials by leading experts. We encourage submission of high-quality papers reporting original work in both theoretical and experimental research areas. Topics of interest include, but are not limited to the following:

- **Routing Protocols**
- **MPLS and GMPLS**
- **Sensor Networks**
- **Quality of Service**
- **Wireless Networks**
- **Optical Networks**
- **Traffic Engineering**
- **Pricing and Charging**
- **Traffic Measurement**
- **Network Management**
- **Protection and Recovery**
- **Performance Evaluation**
- **High Speed Packet Processors**
- **Mobile and Ad-hoc Networking**
- **Switch and Router Architecture**

Papers must be unpublished and must not be submitted for publication elsewhere. All papers will be reviewed by the members of the Technical Program Committee and other experts in the field. Authors of accepted papers must attend the workshop to present their contributions. Papers must be submitted electronically as PDF files and must not exceed 5 pages in two-column IEEE Transactions style. Detailed instructions for paper submission are available on the workshop web site.

Proposals are solicited for tutorials. Please contact the Tutorials Chair for further information

IMPORTANT DATES :

Full papers due on **October 15, 2003**.

Notifications of acceptance distributed by **January 15, 2004**.

Camera-ready papers and registrations are due on **February 15, 2004**.



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